

SCHOOL OF ELECTRICAL & ELECTRONICSENGINEERING

TRADE DIPLOMA IN ENGINEERING (ELECTRICAL & ELECTRONICS MAJOR)

EEE466 - CIRCUIT ANALYSIS

SA/SE/Re-SIT EXAMINATION - PENSTER-1, 2017

DAY/DATE: 06/03/2017 TIME: 9.00-12.00pm DURATION: 3HRS.10min

ROOM: SAMABULA CAMPUS

INSTRUCTION TO STUDENT

- 1. You are allowed 10 minutes extra reading time during which you are NOT to write.
- 2. <u>Begin</u> each answer(each Question) on a fresh page and use both sides of the sheet.
- 3. Write your candidate number at the top of each answer & attached sheet.
- 4. Insert all written foolscaps, graph paper etc. in their correct sequence and secure with a string.
- 5. For all sheets of paper on which rough/draft work has been done, cross it through and you must attach all of them to your answer scripts.
- 6. Write clearly the number(s) of the question(s) attempted on the top of each sheet.
- 7. Tables & formula on the Appendix.
- 8. SECTION A. Multiple Choice & Short Answer-20 marksSECTION B. Answer ALL question-50 marksSECTION C. Select any three(3) question.-30 marks

Section – A:

[20 marks]

Multiple Choice

(12 marks)

In this section, read the question properly/ carefully and select your best answer by writing the alphabet of your choice along the question number in the answer sheet. (1 mark each)

.>

- 1. The value of natural logarithm e is approximately?
 - (a) 2.718
 - (b) 2.728
 - (c) 3.142.
 - (d) 2.782.
- 2. In RC Timing circuits, during the 1st time constant, the capacitor is charged to about --- of the source voltage.
 - (a) 62.3%
 - (b) 63.2%
 - (c) 62.4%.
 - (d) 62.7%.
- 3. What is the *frequency* component of the following voltage equation, $v = 12 \sin 377t + 70^{\circ}$.
 - (a) 497
 - (b) 70
 - (c) 377
 - (d) 377+70
- 4. What is the *phase shift* component of the following voltage equation, $v = 12 \sin 377t + 70^{\circ}$.
 - (a) 377t + 70
 (b) 70
 (c) 377
 - (d) 12
- 5. Determine what *frequency* will an inductor of 5mH will have the same reactance with a capacitor of 0.1uF?
 - (a) 712kHz
 - (b) 7.21kHz
 - (c) 7.12kHz
 - (d) 721kHz.

6. In a RCL series circuit, a 240v supply with 50 Hz connected with a resistor of 20Ω, capacitor of 80µF and inductor of 0.25H. Determine the *inductive* reactance?

.:

- (a) 785.3kΩ
 (b) 785.3Ω
 (c) 78.53kΩ
 (d) 78.53Ω.
- 7. In a RCL series circuit, a 240v supply with 50 Hz connected with a resistor of 20Ω , capacitor of 80μ F and inductor of 0.25H. Find the *capacitive* reactance?
 - (a) 39.79Ω
 (b) 39.79kΩ
 (c) 39.79MΩ
 (d) 39.97Ω.
- 8. In a RCL series circuit, a 240v supply with 50 Hz connected with a resistor of 10Ω , capacitor of 60μ F and inductor of 0.20H. The *impedance* of the circuit will be:-?
 - (a) 42.60Ω
 (b) 13.93Ω
 (c) 43.6kΩ
 (d) 42.60kΩ.
- 9. Convert the following voltage $v_a = 50 \sin(377t + 30^0)$ from <u>time domain</u> into <u>phasor</u> <u>domain</u> and re-write the voltage into phasor format.
 - (a) $v_a = 70.71 v \angle 30^\circ$
 - (b) $v_a = 35.35 v \angle 21^\circ$
 - (c) $v_a = 70.71 v \angle 21^\circ$
 - (d) $v_a = 35.35 v \angle 30^\circ$.

10. Which of the following dependent sources is not appropriate?

- (a) voltage controlled voltage source
- (b) current controlled voltage source
- (c) voltage controlled current sense
- (d) current controlled current source

11. A system of 120V dc delivers 4.6W to the load as per diagram below. Find the <u>peak value</u> of the applied voltage (E_m) and the current (I_m) if the source to delivered same power to the same load.

.2



- (a) $E_m = 149.24v$, $I_m = 42.54mA$
- (b) $E_m = 159.86v$, $I_m = 42.42mA$
- (c) $E_m = 84.85v$, $I_m = 54.21mA$
- (d) $E_m = 169.71v$, $I_m = 54.21mA$
- 12. In a purely capacitive circuit, current (i) leads voltage (v) by 90° , determine the average power in the capacitor.



(a) $P_{av} = 0w$ (b) $P_{av} = 45w$ (c) $P_{av} = 100w$ (d) $P_{av} = 1Kw$

Short Answer

(8 marks)

In this section, read the question properly/ carefully and determine your answer correctly by writing it in the answer sheet provided.

The voltage across a 5.6µF Capacitor is given as v = 35 sin 300t. What is the sinusoidal expression for the current? [2 marks]

.:

2. Name 2 types of dependent power sources.

[2 marks]

3. Determine the average power delivered to networks having the following input voltage and current as described below: - [2 marks]

 $v = 120\sin(\omega t - 60^{\circ})$ $i = 20\sin(\omega t - 45^{\circ})$

4. Find the Effective Values(RMS) of the sinusoidal waveform in graph below:-

[2 marks]



[50 marks]

<u>Question 1</u> - Series/Parallel Resistor Network

(a). A resistive circuit is given below. Workout the *Req* between terminal a and b. (4 marks)

. 3



(b). For the Series/Parallel Resistive network circuit below, find the following:-



i) The voltage across each resistor. (4marks)

- ii) The current through the circuit.
- iii) If there is a short between points c-b, work out the current through the parallel branch (3marks)

iv) The current through the circuit if points a-b is shorted. (2marks)

(c). Find the current flowing through each branch of the circuit in figure below....

(6 marks)

(1mark)



EEE466 - Circuit Analysis SA_SE_Re-SIT Examination Paper, Penster-I Exam Period, - 2017

6

Question 2 - Nodal Analysis

(a). Using Nodal Analysis method, find the voltage at node "a" & node "b" in a circuit given below. (10 marks)

.:



(b). Find the current in each resistor in the circuit below.

(10 marks)



<u>Question 3</u> - Thévenin's Theorem

From the circuit given below, determine Thévenin's equivalent circuit and calculate the current through the load resistor. (10 marks)



Section – C:

[30 marks]

Select any THREE (3) questions from the seven(7) question given below.

3

Question 1

Use complex algebra [j-notation] application to:-

- i. Determine the input impedance of the circuit shown below. [7 marks]
- ii. Plot its phasor diagram [3 marks]



(use $\omega = 10 \text{ rad/s}$)

Question 2

For the circuit below, <u>show the full calculation</u> of the Gain = I_0 (w) / I_I (w) and determine the poles and zeroes of the circuit below using the RL and RC responses. (10 marks)



Question 3.

(a). Determine the impedance and its phase angle of the RL circuit below? (6 marks)

.>



(b). The voltage across a 3uF Capacitor is given as $v = 30 \sin 377t$. What is the sinusoidal expression for the current and Sketch the <u>voltage</u> and <u>current</u> curves. (4 marks)

Question 4

For the RLC circuit below, resistor = 8Ω , capacitor = 0.6uF and inductor = 6mH, show all the necessary calculation to determine the following:- (10 marks)



(use $\pi = 3.14$)

(2 marks)
(4 marks)
(2 marks)
(2 marks)

EEE466 - Circuit Analysis SA_SE_Re-SIT Examination Paper, Penster-I Exam Period, - 2017

9

Question 5

For the circuit shown below, calculate and determine the Norton's equivalent circuit. (10 marks)

.2



Question 6

For the parallel RCL network below.

(10 marks)



(3 marks)

(2 marks)

(2 marks)

 $(1\frac{1}{2} \text{ marks})$

(1½ marks)

- i. Find the admittance of each parallel circuit.
- ii. Determine the input admittance.
- iii. Calculate the input impedance
- iv. Draw the input impedance diagram.
- v. Draw the admittance diagram.

Question 7

Determine the current i_2 for the network in the circuit below. (10 marks)

.>





<u>Appendix 1</u>

3

Formulas

1. $w_x = \frac{R}{2L} + \sqrt{\left(\frac{R}{2L}\right)^2 + \frac{1}{LC}}$ 2. $w_y = -\frac{R}{2L} + \sqrt{\left(\frac{R}{2L}\right)^2 + \frac{1}{LC}}$ 3. $f_0 = \frac{1}{2\pi}\sqrt{LC}$ 4. $Q = \frac{1}{w_0 CR}$ 5. $B = w_0^2 CR$ 6. $X_L = 2\pi fL$ 7. $X_C = \frac{1}{2\pi fC}$ 9. $I_D = I_{DSS} \left(1 - \frac{V_{CS}}{V_P}\right)^2$, $\beta_1 \left[(V_{DD} - 2V_T)V_{ON} - 0.5^2 ON\right]$ 10. $f_H = \pm \frac{8f_0}{V_C}$, $V_{DS} = V_{DD} - I_D R_D$